EUROPRACTICE DESIGN TOOLS INTEL FPGA DONATION ORDER FORM V. 20250626 1 of 3

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Additional Documents (SEE NOTES BELOW)	Submit with this form?
Institute Purchase Order	In all cases where a cost is involved
End User Agreement (EUA)	First order where member has no licenses of any type this vendor via EUROPRACTICE
End Usage Statement (Research Laboratory Members only)	Any order where member is ordering licenses of a new package bundle type

Links to the above documents can be found under the Ordering menu (see "How to Order") in this vendor's EUROPRACTICE web page, accessible via our homepage https://www.europractice.stfc.ac.uk Details on how orders are processed are at https://www.europractice.stfc.ac.uk/tools/how_to_order_details.html

We no longer need to insist on paper copies of documents for this vendor so <u>ALL DOCUMENTS should be e-mailed</u> to <u>MicroelectronicsCentre@stfc.ac.uk</u> <u>A good</u> quality colour scanned PDF copy of the EUA, if applicable, is needed

Missing items will delay placement of your order with the vendor

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EUROPRACTICE Representative

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Licenses		Total Qty License for this order	Annual Charge (Annual Charge will apply on this order if this is your Institute's first order for Intel FPGA licenses via EUROPRACTICE)	(~) Already paying annual charge due to existing licenses (note 1)	Cost EURO Annual Charge	Total Cost EURO Annual Charge
Intel FPGA Core Software			Intel FPGA Annual Charge		220	
Quartus Prime Floating license (includes IP Base Suite)	SW-PE-FLT-UNIV					
Quartus Prime Node Locked Licenses (includes IP Base Suite)	SW-PE-FIX-UNIV		Right to use, upgrades and technical payment, compulsory for each year of	support are p	rovided by ann	nual charge
Additional Core Software and additional IP should be requested on I There is currently no rehost path for Intel FPGA licenses. If licenses request these above and complete the license server details on Page	s are required on a new hostid	please	Annual charges are irrespective of nu 1) Commitment of annual charge for f Design tool Ordering information at: https://www.europractice.stfc.ac.uk/to For advice on who an Institution's EU Institution's current design tools holding support enquiries, please e-mail Micro	mber of licens irst order of a ols/how_to_ol ROPRACTICI ngs, and for a	ses being puro ny new bundle rder_details.ht E Representat II other EURO	chased. (Note e is mandatory.
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LICENSE SERVER INFORMATION

	Server Identification (Note 2)									Server Machine Type & Operating System (Win32 / Linux)	Number of seats of core software requested for this hostid (complete only Floating OR Nodelocked box, NOT both)			
Server Identification (Note 2)				· - ,		Floating Licenses (show Quantity)	OR	Single Node Locked License (please tick ✓)						

Please duplicate this page and use a separate copy for each different license server

In this box please confirm the actual physical location (geographic address) of the above server:	Additional Intel FPGA Software being
	DSP Builder
	Additional Intel FPGA IP being reques
	Protocol IP Bundle
	DSP/Embedded IP Bundle
	Video and Image Processing IP Bundle
	Number of seats of additional Softward core software for the hostid shown ab

Additional Intel FPGA Software being requested on above hostid							
DSP Builder	IPT-DSPB/UNIV						
Additional Intel FPGA IP being requested on above hostid							
Protocol IP Bundle	IP-PROTOCOL/UNIV						
DSP/Embedded IP Bundle	IP-DSPEMB/UNIV						
Video and Image Processing IP Bundle	IP-VIDEO/UNIV						

re and IP will be delivered in same quantity as

Note 2: Server Identification

MAC Address from the Ethernet card:

1. from MSDOS window, type: ipconfig /all PC

2. find the line "Physical address"

Windows: The required ID is 12 Hex characters long (ignore any hyphens)

ip route list | grep default

(find the device name which follows the keyword dev)

PC Linux: ip link show device name | grep ether